



Kib Micro Monitor Instructions

Patrick Vollmar



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subroutines addressing modes interrupts machine cycles and bus timings control signals peripheral I O instructions memory segmentation ag register minimum mode 8086 system and timings assembler directives and operators Interfacing devices data transfer schemes interfacing and I O devices programmable peripheral interface PPI programmable keyboard display interface Intel 8279 centronix parallel communication RS 232C UART programmable interval timer 8253 8254 8257 and 8259 Microprocessor applications seven segment LED display microprocessor based traf c control data acquisition system analog to digital A D converter traf c signal controller digital to analog converter Microprocessor 80XXX architecture pin configuration instructions set addressing modes interrupts multitasking and comparison with different microprocessors Microcontroller 8051 MCS 51 family overview architecture basic registers counters and timers timer counter interrupts serial data input output addressing modes push and pop opcodes instructions set arithmetic operations programming and testing the design real time operatingsystems RTOS ARM AVR and PIC microcontrollers architecture programming model registers and ags exception and interrupt modes instructions set PIC microcontroller family PIC16F84 microcontroller EEPROM data memory PIC16Cxx microcontroller family Embedded systems programming using Keil software instructions set for 8085 8086 and 8051

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