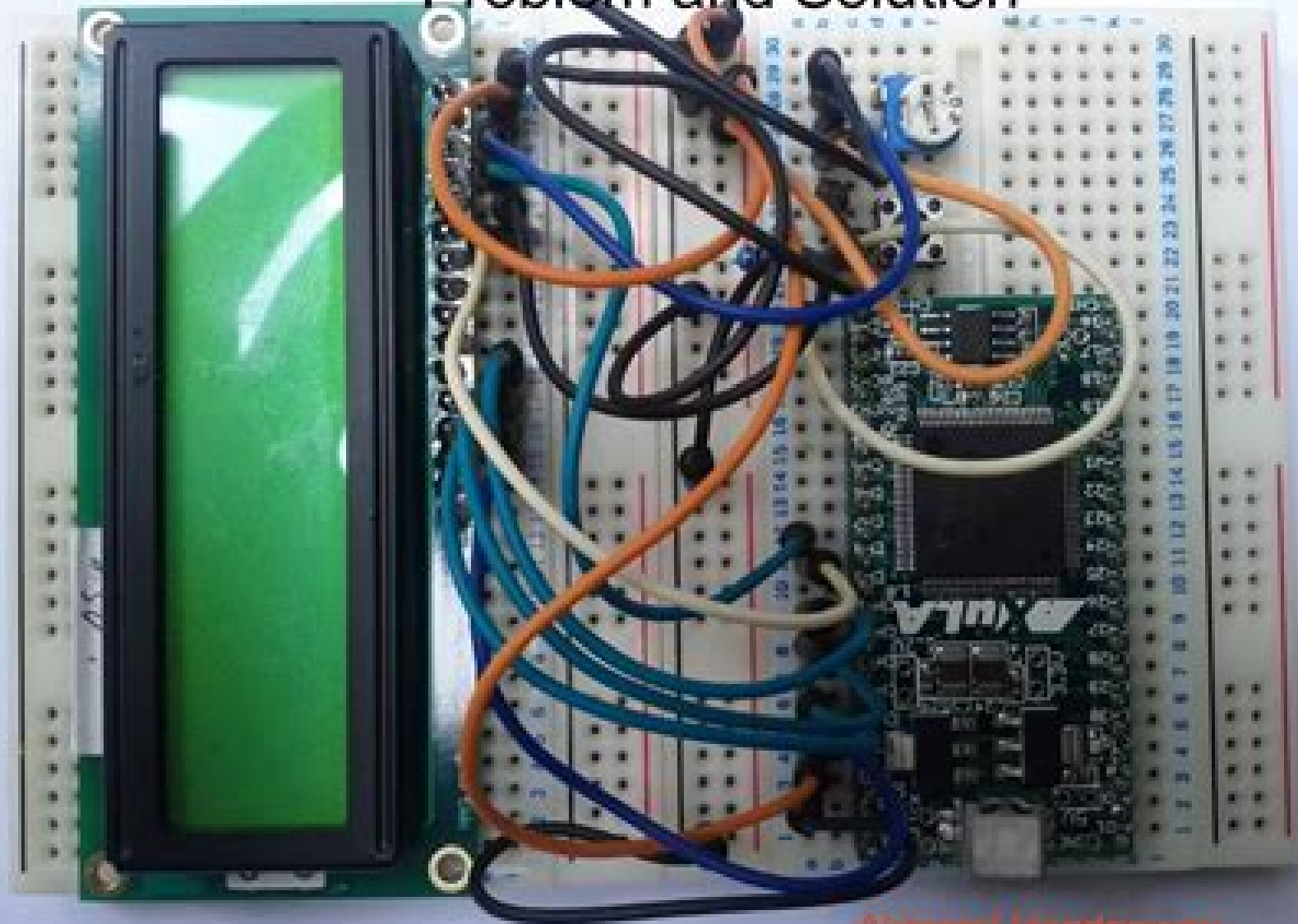


Interfacing FPGA to LCD 16x2 Problem and Solution



Advised Hardware:
henon@wpi.edu

Lcd Interfacing With Fpga Vhdl Code

G Psacharopoulos



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