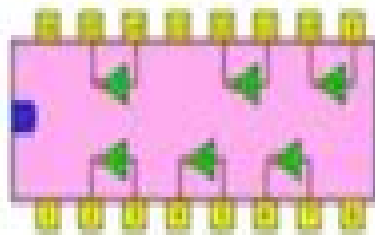
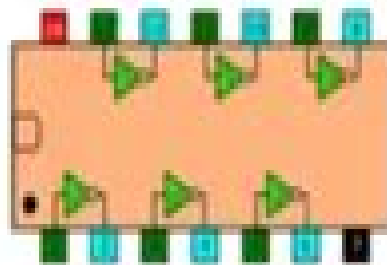


List of Common 4000 Series CMOS ICs



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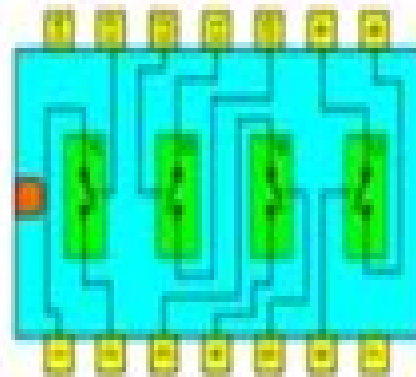
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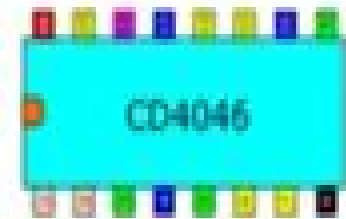
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CD4017



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Joacim Rocklöv



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The ARRL General Class License Manual , *Schaum's Outline of Digital Principles* Roger L. Tokheim, 1994-01-22
Details number systems digital codes logic gates combinational logic circuits TTL and CMOS ICs encoders decoders display drivers LED LCD and and VF seven segment displays flip flops other multivibrators sequential logic counters shift registers semiconductor and bulk storage memories multiplexers demultiplexers latches and buffers digital data transmission magnitude comparators Schmitt trigger devices and programmable logic arrays **Analysis and Design of Digital**

Integrated Circuits David A. Hodges, Horace G. Jackson, 1988 This is a state of the art treatment of the circuit design of digital integrated circuits It includes coverage of the basic concepts of static characteristics voltage transfer characteristics noise margins fanout power dissipation and dynamic characteristics propagation delay times and the interrelationships among these parameters The authors are regarded as leading authorities in integrated circuits and MOS technology

License Manual 77th Ed/2nd Pr Wyland Dale Clift, 1981 **A User's Handbook of Integrated Circuits** Eugene R. Hnatek, 1973 **Computers & Electronics** , 1983 EDN , 1984 **Digital Electronics** Anil K. Maini, 2007-09-27 The fundamentals and implementation of digital electronics are essential to understanding the design and working of consumer industrial electronics communications embedded systems computers security and military equipment Devices used in applications such as these are constantly decreasing in size and employing more complex technology It is therefore essential for engineers and students to understand the fundamentals implementation and application principles of digital electronics devices and integrated circuits This is so that they can use the most appropriate and effective technique to suit their technical need This book provides practical and comprehensive coverage of digital electronics bringing together information on fundamental theory operational aspects and potential applications With worked problems examples and review questions for each chapter Digital Electronics includes information on number systems binary codes digital arithmetic logic gates and families and Boolean algebra an in depth look at multiplexers de multiplexers devices for arithmetic operations flip flops and related devices counters and registers and data conversion circuits up to date coverage of recent application fields such as programmable logic devices microprocessors microcontrollers digital troubleshooting and digital instrumentation A comprehensive must read book on digital electronics for senior undergraduate and graduate students of electrical electronics and computer engineering and a valuable reference book for professionals and researchers *MIPS R4000 User's Manual* Joe Heinrich, 1993 For anyone interested in MIPS R4000 and R4400 RISC microprocessors This comprehensive reference manual describes the MIPS R4000 and R4400 family of RISC microprocessors including the 32 bit and the new 64 bit architecture and instruction set Describes the implementation specific interfaces and architectural features of the highly integrated 64 bit R4000 and R4400 MIPS RISC processors and discusses the MIPS RISC Instruction Set Architecture ISA including the 64 bit extensions of the ISA **The ARRL Handbook for the Radio Amateur** , 2001 *The ARRL Handbook*

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- 1 EXL Sim2002 logic simulator EXL Sim2002 is a full featured interactive schematic capture and simulation program that is ideally suited for use with the text at either the entry or advanced level of logic design Its many features include drag and drop capability rubber banding mixed logic and positive logic simulations macro generation individual and global or randomized delay assignments connection features that eliminate the need for wire connections schematic page sizing and zooming waveform zooming and scrolling a variety of printout capabilities and a host of other useful features
- 2 BOOZER logic minimizer BOOZER is a software minimization tool that is recommended for use with the text It accepts entered variable EV or canonical 1 s and 0 s data from K maps or truth tables with or without don t cares and returns an optimal or near optimal single or multi output solution It can handle up to 12 functions Boolean functions and as many inputs when used on modern computers
- 3 ESPRESSO II logic minimizer ESPRESSO II is another software minimization tool widely used in schools and industry It supports advanced heuristic algorithms for minimization of two level multi output Boolean functions but does not accept entered variables It is also readily available from the University of California Berkeley 1986 VLSI Tools Distribution
- 4 ADAM design software ADAM for Automated Design of Asynchronous Machines is a very powerful productivity tool that permits the automated design of very complex asynchronous state machines all free of timing defects The input files are state tables for the desired state machines The output files are given in the Berkeley format appropriate for directly programming PLAs ADAM also allows the designer to design synchronous state machines timing defect free The options include the lumped path delay LPD model or NESTED CELL model for asynchronous FSM designs and the use of D FLIP FLOPs for synchronous FSM designs The background for the use of ADAM is covered in Chapters 11 14 and 16 of the REVISED 2nd

Edition 5 A OPS design software A OPS for Asynchronous One hot Programmable Sequencers is another very powerful productivity tool that permits the design of asynchronous and synchronous state machines by using a programmable sequencer kernel This software generates a PLA or PAL output file in Berkeley format or the VHDL code for the automated timing defect free designs of the following a Any 1 Hot programmable sequencer up to 10 states b The 1 Hot design of multiple asynchronous or synchronous state machines driven by either PLDs or RAM The input file is that of a state table for the desired state machine This software can be used to design systems with the capability of instantly switching between several radically different controllers on a time shared basis The background for the use of A OPS is covered in Chapters 13 14 and 16 of the REVISED 2nd Edition **EDN, Electrical Design News** ,1985 **ASIC & EDA** ,1993 *The ARRL Handbook for Radio Amateurs*, 2003 American Radio Relay League,2002 Includes a searchable index of QST product reviews a database on over 1000 equipment and parts suppliers and several other programs

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