

```

        TR3(:,j) = pfor
    end
    TR3(:,end) = pfor; %%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%
    for n = 1:blocks
        tr3(n,:) = sqrt(N)*ifft(TR3(n,:))
    end
    tr3 = [tr3(:,(N-L+1):N) tr3]; %blocks*(N+L)
    j = 1;
    Npc = round(N/Nf3)+1; %2A~p~u~y Npc = (N-1)/Nf3+1; %%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%
    %for k = 1:N
        %if (mod(k-1,Nf3)~=0)
            %ss3(j:(Npc-1)*(Nf3-1)+1:j+(blocks-1)*(Npc-1)*(Nf3-1)) = TR3(:,k);
            %uAp~y~u~u~A~A~u~A~y~u~A~A
            %j = j+1;
        %end
    %end
    for k = 1:N-1%
        if (mod(k-1,Nf3)~=0)%
            %ss3(j:(Npc-1)*(Nf3-1)+1:j+(blocks-1)*(Npc-1)*(Nf3-1)+1)) = TR3(:,k);
            %uAp~y~u~u~A~A~u~A~y~u~A~A
            %j = j+1;%
        end%
    end%
    ss3(j:(Npc-1)*(Nf3-1)+1:j+(blocks-1)*(Npc-1)*(Nf3-1)+1)) = TR3(:,N-1); %

    %delayTime = [0 0.2 0.6 1.6 2.4 5.0]*10^(-6); %%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%
    %delayn = delayTime/chipT; %%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%
    %averPower = %%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%

    %channel %%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%
    pns = pns(L+1:pL);
    %%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%
    delayn = 0:25; % [0 1 2 3]; % [0 1 3 8 12 25];
    %averPower = [0.189 0.379 0.229 0.095 0.060 0.038]; [0.575 0.362 0.057 0.006];
    snrdb = 0:5:25;
    for m = 1:length(snrdb)
        %%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%%channel model
        h = zeros(N,blocks); %
        for k=1:M%
            h(delayn(k)+1,:) = randn(1,blocks)+sqrt(-1)*randn(1,blocks); %
        end%

        for k = 1:M%
            h(delayn(k)+1,:) =
            h(delayn(k)+1,:)/norm(h(delayn(k)+1,:))*sqrt(blocks); %
            %'eO~BA~A N*blocks
        end%
        %for k = 1:M
            %h(delayn(k)+1,:) = sqrt(averPower(k))*h(delayn(k)+1,:);
        %end
        for n = 1:blocks
            h(:,n) = h(:,n)/norm(h(:,n)); %'eO~BA~A
        end
        HL = fft(h); % N*blocks
        H(:, :, m) = HL';
        Hs = reshape(H(:, :, m), 1, N*blocks);
    end

```

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Rachel Sandford



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International Conference on Intelligent Computing and Applications Subhransu Sekhar Dash, Swagatam Das, Bijaya Ketan Panigrahi, 2017-12-28 The book is a collection of best papers presented in International Conference on Intelligent Computing and Applications ICICA 2016 organized by Department of Computer Engineering D Y Patil College of Engineering Pune India during 20-22 December 2016. The book presents original work, information techniques and applications in the field of computational intelligence, power and computing technology. This volume also talks about image language processing, computer vision and pattern recognition, machine learning, data mining and computational life sciences management of data including Big Data and analytics, distributed and mobile systems including grid and cloud infrastructure.

Index to IEEE Publications Institute of Electrical and Electronics Engineers, 1998 Issues for 1973 cover the entire IEEE technical literature.

Proceedings of the 2009 International Conference on Signals, Systems and Automation (ICSSA 2009) Himanshu Soni, 2010-04-30 This book is a collection of papers from the 2009 International Conference on Signals, Systems and Automation ICSSA 2009. The conference at a glance: Pre-conference Workshops/Tutorials on 27th Dec 2009; Five Plenary talks; Paper/Poster Presentation 28-29 Dec 2009; Demonstrations by SKYVIEW Inc, SLS Inc, BSNL Baroda Electric Meters, SIS. On-line paper submission facility on website. 200 papers are received from India and abroad. Delegates from different countries including Poland, Iran, USA. Delegates from 16 states of India. Conference website is seen by more than 3000 persons across the world, 27 countries and 120 cities.

Interference Mitigation for Multi-band OFDM Using Diversity Combining and Erasure Based Methods Eric Paul Reinbold, 2005

Mobile Lightweight Wireless Systems Javier Del Ser, Eduard Axel Jorswieck, Joaquin Miguez, Marja Matinmikko, Daniel P. Palomar, Sancho Salcedo Sanz, Sergio Gil-Lopez, 2012-04-23 This book constitutes the thoroughly refereed post-conference proceedings of the Third International ICST Conference Mobile Lightweight Wireless Systems MOBILIGHT 2011 held in Bilbao, Spain on May 9-10, 2011. In numbers MOBILIGHT 2011 was organized as a 2-day single track event with 18 technical presentations and 3 specialized workshops focused on opportunistic sensing and processing in mobile wireless sensor and cellular networks, MOBISense multimode wireless access networks, MOWAN and strategic network planning applied to market regulation, NETSTRAT totaling to 34 papers presented during the conference and included in the proceedings.

Digital Design of Signal Processing Systems Shoab Ahmed Khan, 2011-07-28 Digital Design of Signal Processing Systems discusses a spectrum of architectures and methods for effective implementation of algorithms in hardware. HW Encompassing all facets of the subject, this book includes conversion of algorithms from floating point to fixed point format, parallel architectures for basic computational blocks, Verilog Hardware Description Language (HDL) SystemVerilog and coding guidelines for synthesis. The book also covers system level design of Multi-Processor System on Chip (MPSoC), a consideration of different design methodologies including Network on Chip (NoC) and Kahn Process Network (KPN) based connectivity among processing elements. A special emphasis is

placed on implementing streaming applications like a digital communication system in HW Several novel architectures for implementing commonly used algorithms in signal processing are also revealed With a comprehensive coverage of topics the book provides an appropriate mix of examples to illustrate the design methodology Key Features A practical guide to designing efficient digital systems covering the complete spectrum of digital design from a digital signal processing perspective Provides a full account of HW building blocks and their architectures while also elaborating effective use of embedded computational resources such as multipliers adders and memories in FPGAs Covers a system level architecture using NoC and KPN for streaming applications giving examples of structuring MATLAB code and its easy mapping in HW for these applications Explains state machine based and Micro Program architectures with comprehensive case studies for mapping complex applications The techniques and examples discussed in this book are used in the award winning products from the Center for Advanced Research in Engineering CARE Software Defined Radio 10 Gigabit VoIP monitoring system and Digital Surveillance equipment has respectively won APICTA Asia Pacific Information and Communication Alliance awards in 2010 for their unique and effective designs *A Low-complexity Tentative Decision Interference Suppression Scheme for Multi-band OFDM* Meng Li,2005 **Blind, Adaptive Equalization for Multicarrier Receivers** Richard K. Martin,2004

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