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B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2012.

Sixth Semester

Electronics and Communication Engineering

680290040 — MICROCONTROLLERS AND RISC ARCHITECTURE

(Regulation 2008)

Time: Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — $(10 \times 2 = 20 \text{ marks})$

- How does the pin EA of 8051 help in distinguishing the address range of program memory?
- Show the circuit used for power down operation of 8051.
- What are the data types and operand addressing supported in 8051?
- 4. The carry flag is originally set. The data present at the input pert 3 is C5H. The data previously written to output port 1 is 35H calculate the port 1 contents after the execution of the following instructions.

MOV Pl.3, C

MOV C, P3.3

MOV P1.2 C

- 5. How do you generate the data corresponding to the step angle required in stepped motor interfacing?
- Find an application that uses the auto reload mode of timer 2 in 8051. Justify your answer.
- List the features of ARM architecture that are not shared by most other RISC architectures.
- Give an example for pre-indexed addressing mode of ARM.
- Tabulate the arm- exceptions with their vector address.
- 10. What are the common sources of memory fault allowed by ARM processors?

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scienti c events and its traditional role of serving as a meeting point between East and West EDCC 5 was the fth in the series of these high quality scienti c conf ences In addition to the overall signi cance of such a pan European event this year s conference was a special one due to historic reasons The roots of EDCC date back to the moment when the Iron Curtain fell Originally two groups of scientists from di erent European countries in Western and Eastern Europe who were active in research and education related to dependability created a joint forum in order to merge their communities as early as in 1989 This trend has continued up to today This year's conference was the rst one where the overwhelming majority of the research groups belong to the family of European nations united in the European Union During the past 16 years we observed that the same roots in all the professional cultural and scientic senses led to a seamless integration of these research communities previously separated ar cially for a long time EDCC has become one of the main European platforms to exchange new searchideasinthe eldofdependability Real-Time Programming 1992 J.A. De La Puente, L. Boullart, 2017-02-21 The 47 papers in this volume provide a useful reference tool for the state of the art research in real time FPGA ... ,1999 F & S Index United States Annual ,1995 **Electrical & Electronics Abstracts** ,1997 programming Documentation Abstracts ,1991 **Embedded Systems Programming** ,1999-07 Circuit Cellar Ink ,1993 Electronic Engineering, 1996 **Choice** ,2001 Electronic Products Magazine ,1988 Applied Science & Technology Index ,1996 The New York Times Index ,1990 **ANALYSIS OF MICROCONTROLLERS: ntroduction** toMicroprocessors and Microcontrollers; CH:2 Architecture OF 8051; CH:3 8051 Instruction Set; CH:4 8051 Interfacing with Peripherals; CH:5 Sample Project with 8051 and Introduction to PIC Microcontrollers; CH:6 Introduction to ARM ArchitectureL; CH:7 ARM and THUMB Instruction Sets; ANSWERS TO QUESTIONS; MODEL **QUESTION PAPER; Index** J. P. KOTHARI,2018 Design of an 8 - Bit RISC Microcontroller by Using Harvard **Architecture** Zeng Hoe See,2005

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