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Architecture de l'ordinateur

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Abdelaziz M'zoughi

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→ L1/L2

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+ exos
corrigés**

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Patrick Vollmar



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Mini manuel d'architecture de l'ordinateur Jacques Jorda, Abdelaziz M'zoughi, 2012-02-08 Ce Mini manuel couvre les notions fondamentales qui sont enseignées aux étudiants des L1 et L2 d'informatique. Il débute par l'explication de la façon dont les nombres sont codés dans un ordinateur et comment les opérations de base sont calculées. Il se poursuit par l'étude des fonctions logiques qui permettent des traitements plus complexes puis par les problèmes de synchronisation des opérations à l'aide d'horloges internes. Il se termine par la description d'une unité centrale et par les mécanismes de programmation en langage assembleur. Il comporte une partie cours et une partie exercices corrigés. 1 3

DSCG 5 - Management des systèmes d'information Manuel et applications 5e édition

Jean-Pierre Marca, Philippe Germak, Jean-François Soutenain, 2017-09-06 Tout le programme de cours de la preuve n° 5 Management des systèmes d'information dans un manuel qui propose également des applications. L'ouvrage entièrement remanié et structuré pour la présentation du concept de gouvernance informatique, description approfondie des architectures matérielles et logicielles en usage aujourd'hui, expose des fondamentaux de la conduite de projets SI, développement sur les problématiques d'intégration et les outils de gouvernance informatique, initiation à la démarche de l'auditeur. Un cours structuré et illustré. Pour s'entraîner à la preuve d'examen, nombreux exercices de cas complétés, inspirés de situations d'entreprises réelles. L'ouvrage s'accompagne d'un manuel de la preuve avec corrigés commentés. Un ouvrage complet, cours, entraînement et applications pour réussir son examen n° 5 de Management des systèmes d'information. div

Les Livres disponibles, 2002 La liste exhaustive des ouvrages disponibles publiés en langue française dans le monde. La liste des auteurs et la liste des collections de langue française.

Architecture de l'ordinateur Andrew Tanenbaum, 2006-03-17 Ouvrage de référence dans tous les pays où il a été publié. Le livre d'Andrew Tanenbaum en est maintenant sa cinquième édition. L'auteur y analyse les aspects matériels et logiciels de l'architecture des ordinateurs, part du matériel en couches qu'il a développé. Ce modèle permet de mettre en évidence les six couches fondamentales qui composent les systèmes informatiques actuels, allant de la couche physique (les composants électroniques) jusqu'à la couche application (les programmes des utilisateurs), en passant par les couches microarchitecture ISA, système d'exploitation et langage d'assemblage. Pour illustrer son propos, Andrew Tanenbaum applique son analyse à trois machines les plus courantes : le Pentium 4, l'UltraSPARC III de Sun et le microcontrôleur 8051 d'Intel, ainsi qu'à trois systèmes d'exploitation types du marché : Windows, Unix et Linux. Un chapitre entier est par ailleurs consacré aux évolutions de l'architecture des ordinateurs, il présente notamment les architectures parallèles et massivement parallèles, ainsi que les systèmes répartis. On notera plus particulièrement une présentation de nouveaux dispositifs : les E-S, les appareils photos numériques et DSL, la prise en compte du bus PCI Express, le traitement des systèmes embarqués, les caractéristiques du 8051, la microarchitecture NetBurst du Pentium 4. Deux annexes : l'une sur la représentation des nombres binaires, l'autre sur l'expression des nombres en virgule flottante. C'est un bon ouvrage pour sa pédagogie, riche en exemples et illustrations, ce manuel comprend près de 300 exercices d'application.

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Le Synthex Architecture de l'ordinateur présente la structure des ordinateurs travers l'étude de ses composants fondamentaux : le processeur, la mémoire et les entrées/sorties et périphériques *Bibliographie du Québec*, 1993 Bulletin signalétique, 1976 Un an de nouveautés, 1980 *Bulletin signalétique*, 1974 *Point de repère*, 2001 Annales ... Institut technique du bâtiment et des travaux publics, Paris, 1971 **Le Moniteur architecture**, 1991

Le microprocesseur 1 Philippe Darche, 2021-06-01 Depuis 50 ans le microprocesseur forme moderne et intégrée de l'unité centrale n'a cessé d'évoluer en termes d'intégration de fonctions, de puissance de calcul, de baisse de prix et d'économie d'énergie. Il est aujourd'hui présent dans la quasi-totalité des appareils électroniques. Bien connaître ses mécanismes internes et sa programmation est essentiel pour comprendre et maîtriser le fonctionnement d'un ordinateur et les concepts liés de programmation. Le microprocesseur 1 traite des deux premières générations de microprocesseurs c'est-à-dire celles qui manipulent les entiers aux formats de 4 et 8 bits. Ce volume présente la fonction de calcul, rappelle celle de mémorisation et précise les notions de modèle de calcul et d'architecture des ordinateurs. Des exemples puisés dans les technologies actuelles et anciennes illustrent et rendent accessibles les concepts théoriques **Le Moniteur Architecture - AMC.**, 1991

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