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Durat	tion: 3 Hrs. M Marke:	75
	Section A	10
Q1.	Do as directed:	2.5
0.	- CONTROL OF STREET AND STREET AN	1100
b.	PAD in equal to 1000 bits (T/F)	
G.	THE PROPERTY OF STREET PROPERTY AND THE PROPERTY OF STREET	
d.	9000 microprocessor chip has number of plan	
	Stack works on the principle of last in last out /T/F)	
1.	8085 microprocessor has general purpose 8 bit registers	
9	In used as a memory pointer for the etack memory	
h.	MVI instruction is an example of addressing mode	
1.	The antinmetic operations implicitly assume that the contents of the assume to	ner est
	the operands (1/F)	
3-	Mov is a byte instruction.	
	Section B	
Worter:	Attempt any five questions	Sec. 10
221.	What do you mean by instruction cycle? Explain	ixt5
W.,	Explain various status flags in 8085	
Nt.	Write a program to ad the two hexadecimal numbers 3 AH and 48 H and to display	eller en
100	arkiwer at an output port.	Edit Helia
èv.	How 8085 instructions are classified according to byte size? Explain.	
W.	Describe the interrupt driven data transfer echange	
Wi.	Compare 8085 with 6800 microprocessor.	
	Section C	
lote:	Attempt any three questions 10	w 78
23.	Explain the various addressing modes with example.	A
3-4.	Draw the functional block diagram of 8085 and describe the function of each block in br	
15.	List various data transfer schemes. Explain DMA data transfer.	Heart.
16	Write short note on	
-	Subroutines	
b	8085 interrupts	
100	Community of the state of the s	

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