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Question Paper Code : 21266

B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2012.

Sixth Semester

Electronics and Communication Engineering

680290040 — MICROCONTROLLERS AND RISC ARCHITECTURE

(Regulation 2008)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. How does the pin \overline{EA} of 8051 help in distinguishing the address range of program memory?
2. Show the circuit used for power down operation of 8051.
3. What are the data types and operand addressing supported in 8051?
4. The carry flag is originally set. The data present at the input port 3 is C5H. The data previously written to output port 1 is 30H calculate the port 1 contents after the execution of the following instructions.

MOV P1.3, C

MOV C, P3.3

MOV P1.2, C

5. How do you generate the data corresponding to the step angle required in stepped motor interfacing?
6. Find an application that uses the auto - reload mode of timer 2 in 8051. Justify your answer.
7. List the features of ARM architecture that are not shared by most other RISC architectures.
8. Give an example for pre-indexed addressing mode of ARM.
9. Tabulate the arm- exceptions with their vector address.
10. What are the common sources of memory fault allowed by ARM processors?

Microcontroller And Risc Architecture Question Paper

Lingsheng Yao



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Table of Contents Microcontroller And Risc Architecture Question Paper

1. Understanding the eBook Microcontroller And Risc Architecture Question Paper
 - The Rise of Digital Reading Microcontroller And Risc Architecture Question Paper
 - Advantages of eBooks Over Traditional Books
2. Identifying Microcontroller And Risc Architecture Question Paper
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Microcontroller And Risc Architecture Question Paper
 - User-Friendly Interface
4. Exploring eBook Recommendations from Microcontroller And Risc Architecture Question Paper
 - Personalized Recommendations

- Microcontroller And Risc Architecture Question Paper User Reviews and Ratings
- Microcontroller And Risc Architecture Question Paper and Bestseller Lists
- 5. Accessing Microcontroller And Risc Architecture Question Paper Free and Paid eBooks
 - Microcontroller And Risc Architecture Question Paper Public Domain eBooks
 - Microcontroller And Risc Architecture Question Paper eBook Subscription Services
 - Microcontroller And Risc Architecture Question Paper Budget-Friendly Options
- 6. Navigating Microcontroller And Risc Architecture Question Paper eBook Formats
 - ePub, PDF, MOBI, and More
 - Microcontroller And Risc Architecture Question Paper Compatibility with Devices
 - Microcontroller And Risc Architecture Question Paper Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Microcontroller And Risc Architecture Question Paper
 - Highlighting and Note-Taking Microcontroller And Risc Architecture Question Paper
 - Interactive Elements Microcontroller And Risc Architecture Question Paper
- 8. Staying Engaged with Microcontroller And Risc Architecture Question Paper
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Microcontroller And Risc Architecture Question Paper
- 9. Balancing eBooks and Physical Books Microcontroller And Risc Architecture Question Paper
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Microcontroller And Risc Architecture Question Paper
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Microcontroller And Risc Architecture Question Paper
 - Setting Reading Goals Microcontroller And Risc Architecture Question Paper
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Microcontroller And Risc Architecture Question Paper
 - Fact-Checking eBook Content of Microcontroller And Risc Architecture Question Paper

- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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