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MICROPROCESSOR

4" Exam/Comp/6269/Dec 09

	- Camillo Con Dec Us	
Dura	tion: 3 Hrs. M.Marks:	77.00
	Section A	7.0
Q1.	Do as directed:	15
0.	The state of the s	1.00
b.	TND is equal to 1000 bits (T/F)	
	THE PROPERTY OF A SECURIOR STATES OF THE PROPERTY OF THE PROPE	
d.	9090 Microprocessor chip has number of plan	
0.	Stack works on the principle of last in last out (T/C)	
	8085 microprocessor has general purpose 8 bit registers	
9	IS USED as a memory pointer for the etack manner.	
h.	MVI instruction is an example of addressing mode	
L	The antinmetic operations implicity assume that the contents of the anti-	ne of
	trie operands (1/)-)	
1	Mov is a byte instruction.	
	Section B	
Note:	Attempt any five questions	
Q21.	What do you mean by instruction cycle? Explain.	SX5
W.,	Explain various status flags in 8085.	
761	Write a program to ad the two hexadecimal numbers 2 AN and 48 N and 48 N	100
	answer at an output port.	tine
ève.	How 8085 instructions are classified according to byte size? Explain.	
W.	Describe the interrupt driven data transfer echange	
Wi.	Compare 8085 with 6800 microprocessor.	
	Section C	
Note:	Attempt any three guestions	
Q3.	Explain the various addressing modes with example.	bc3
Q4.	Draw the functional block diagram of 8085 and describe the function of each block in b	
Q5.	List various data transfer schemes. Explain DMA data transfer.	men.
OB	Write short note on	
a a	Subroutines	
b	8085 interrupts	

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Rachel S Tattersall

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