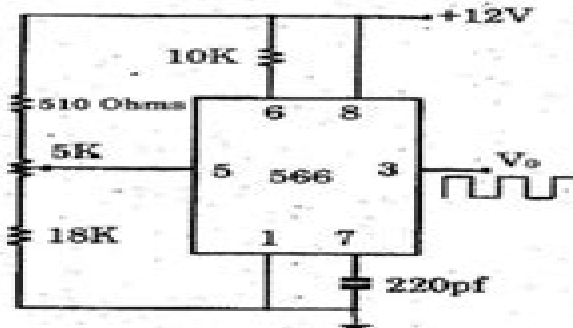


13. (a) Explain the operation of Dual slope ADC with neat illustrations? Also prove that this ADC is free from drifts? (10 + 6)

Or

- (b) With neat schematic representations explain the operation of the following circuits?
- Positive Peak follower.
  - Active positive clamper to clamp the input signal above ground state by 5 V. (8 + 8)
14. (a) Explain with functional block diagram the operation of 566 Voltage Controlled Oscillator. Also determine the maximum and minimum output frequencies in the circuit shown below. (10 + 6)



Or

- (b) Explain the operation of an astable multi-vibrator configured around 555 Timer IC and derive an expression for output frequency with neat illustration.
15. (a) Design a Voltage regulator using IC 723 regulator to satisfy the following specifications
- $V_o = 12 \text{ V}$ .
  - $I_o = 500 \text{ mA}$ .
  - $V_{in} = 18 \pm 20\%$ .
  - $I_{sc} = 600 \text{ mA}$ .
  - $V_{sense} = 0.7 \text{ V}$ . Give the complete schematic diagram. (Assume and Justify if any data required)

Or

- (b) Discuss in detail the operation and applications of the following circuits.
- Isolation amplifier
  - Optocoupler.

# Linear Integrated Circuit Sample Question Paper

**Prithviraj Kabisatpathy, Alok  
Barua, Satyabroto Sinha**



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26 DFCCIL CBT 2 Executive Signal Telecommunication Solved Papers Practice Book 240 495 E This book contains 4 sets of previous year solved papers and 10 sets of practice book      **Fault Diagnosis of Analog Integrated Circuits** Prithviraj Kabisatpathy,Alok Barua,Satyabroto Sinha,2006-01-13 System on Chip SOC having both digital and analog circuits has become increasingly prevalent in integrated circuit manufacturing industry Electronic tests are classified as digital analog and mixed signal Current methodologies for the testing of digital circuits are well developed In contrast methodologies for the testing of analog circuits remain relatively underdeveloped due to the complex nature of analog signals Compared to digital testing analog testing lags far behind in methodologies and tools and therefore demands substantial research and development effort Fault Diagnosis of Analog Integrated Circuits is a textbook for advanced undergraduate and graduate level students as well as practicing engineers The objective of this book is to study the testing and fault diagnosis of analog and analog part of mixed signal circuits A background in analog integrated circuit artificial neural network is desirable but not essential The text covers the testing and fault diagnosis of both bipolar and Metal Oxide Semiconductor MOS circuits Fault model of the devices in analog domain has been introduced in the text The test stimulus generations are also discussed in details Experimental verification of some state of the art techniques has also been presented in the book It also contains problems that can be used as quiz or homework This book enables the reader to test an analog circuit that is implemented either in bipolar or MOS technology      Linear Integrated Circuits Frederick Gans,1970      **2025-26 RRB JE Stage-II Electronics & Allied Engineering Solved Papers and Practice Book .** YCT Expert Team , 2025 26 RRB JE Stage II Electronics Allied Engineering Solved Papers and Practice Book 304 595 E This book contains 15 sets of the previous year solved papers and practice book      *BSNL Jr. Engineer (TTA) Exam Guide + Practice Workbook (Concept Notes + 2 Solved + 10 Practice Sets) 2nd Edition* Disha Experts,2017-09-01 The book BSNL TTA Exam Guide Practice Workbook Concept Notes 2 Solved 10 Practice Sets 2nd Edition has been specially designed to help students in the BSNL TTA exam Two fully solved past paper have been provided to guide you about the pattern and the level of questions asked The book covers theory material for Basic Engineering and Specilization Section to help in the preparation It also contains 2 past papers and 10 Practice Sets as per the pattern Each Practice Set is classified into 3 parts General Ability Test This part have 20 questions Basic Engineering This part have 90 questions and Specialization This part have 90 questions The questions in each practice set have been carefully selected so as to give you a real feel of the exam The book provides Response Sheet for each test Post each test you must do a Post Test Analysis with the help of the Test Analysis and Feedback Sheet which has been provided for each test      **WESCON Technical Papers** ,1968      Design of Analog Integrated Circuits and Systems Kenneth R. Laker,Willy M. C. Sansen,1994 It follows with a thorough treatment of design operational and operational transconductance

amplifiers and concludes with a unified presentation of sample data and continuous time signal processing systems

**Selected Papers on Statistical Design of Integrated Circuits** Andrzej J. Strojwas, 1987      **Book of ASTM Standards, with Related Material** American Society for Testing and Materials, 1982      **1968 Wescon Technical Papers**, 1968      **IEEE VLSI Test Symposium**, 2005      *Annual Book of ASTM Standards* ASTM International, American Society for Testing and Materials, 2003      *Bell Laboratories Talks and Papers* Bell Telephone Laboratories. Libraries and Information Systems Center, 1984      **IEEE, ACM International Conference on Computer Aided Design** International Conference on Computer Aided Design. Institute of Electrical and Electronics Engineers, 2000, San José, Calif., 2000      **A Volume of Technical Papers Presented at AIAA/ION Guidance & Control Conference**, 1965      *Proceedings of the ... Midwest Symposium on Circuits and Systems*, 1997      *Journal of the Institution of Water Engineers and Scientists* Institution of Water Engineers and Scientists, 1978      *Proceedings of the 34th Midwest Symposium on Circuits and Systems* Sherif Michael, 1991      **Summaries of Papers Presented at the Conference of Lasers and Electro-optics**, 2002  
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