

Eldo[®] Platform User's Manual



Mentor Graphics Eldo User Manual

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MOSFET Modeling & BSIM3 User's Guide Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike *ISTFA 2012* ASM International, EDFAS Organizing Committee, editors, 2012 *Extreme Environment Electronics* John D. Cressler, H. Alan Mantooth, 2017-12-19 Unfriendly to conventional electronic devices circuits and systems extreme environments represent a serious challenge to designers and mission architects The first truly comprehensive guide to this specialized field *Extreme Environment Electronics* explains the essential aspects of designing and using devices circuits and electronic systems intended to operate in extreme environments including across wide temperature ranges and in radiation intense scenarios such as space The *Definitive Guide to Extreme Environment Electronics* Featuring contributions by some of the world's foremost experts in extreme environment electronics the book provides in depth information on a wide array of topics It begins by describing the extreme conditions and then delves into a description of suitable semiconductor technologies and the modeling of devices within those technologies It also discusses reliability issues and failure mechanisms that readers need to be aware of as well as best practices for the design of these electronics Continuing beyond just the paper design of building blocks the book rounds out coverage of the design realization process with verification techniques and chapters on electronic packaging for extreme environments The final set of chapters describes actual chip level designs for applications in energy and space

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Proceedings of the IEEE 1999 Custom Integrated Circuits Conference ,1999 **Records of the 2002 IEEE**

International Workshop on Memory Technology, Design and Testing Bernard Courtois, Thomas Wik, Yervant Zorian, 2002 Annotation MTDT 2002 explores the state of the art in semiconductor memories Over the last 10 years the scope of the Workshop has been expanded to cover the fabrication technology and the memory design test and reliability

Proceedings ,2005 *Analog and Mixed-signal Test* Bapiraju Vinnakota, 1998 More and more chips are being designed with both analog and digital circuitry next to each other which makes testing analog circuitry even more challenging This comprehensive guide reviews all the potential testing options helping designers engineers CAD developers and researchers choose the most cost effective accurate solutions for both mixed signal and analog only testing **Proceedings of the ...**

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