VLSI Digital Signal Processing Systems

Design and Implementation

Keshab K. Parhi

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IEEE Circuits & Devices ,2000   VLSI Synthesis of DSP Kernels Mahesh Mehendale,Sunil D. Sherlekar,2013-04-17 A critical step in the design of a DSP system is to identify for each of its components DSP kernels an implementation architecture that provides the desired degree of flexibility programmability and optimises the area delay power parameters The book covers the entire solution space comprising both hardware multiplier based and multiplex less architectures that offer varying degrees of programmability For each of the implementation styles several algorithmic and architectural transformations are proposed so as to optimally implement weighted sum based DSP kernels over the area display power space VLSI Synthesis of DSP Kernels presents the following Six different target implementation styles Programmable DSP based implementation Programmable processors with no dedicated hardware multiplier Implementation using hardware multiplier s and adder s Distributed Arithmetic DA based implementation Residue Number System RNS based implementation and Multiplier less implementation using adders and shifters for fixed coefficient DSP kernels For each of the implementation styles description and analysis of several algorithmic and architectural transformations aimed at one or more of reduced area higher performance and low power Automated and semi automated techniques for applying each of these transformations and Classification of the transformations based on the properties that they exploit and their encapsulation in a design framework A methodology that uses the framework to systematically explore the application of these transformations depending on the characteristics of the algorithm and the target implementation style VLSI Synthesis of DSP Kernels is essential reading for designers of both hardware and software based DSP systems developers of IP modules for DSP applications EDA tools developers researchers and managers interested in getting a comprehensive overview of current trends and future challenges in optimal implementations of DSP kernels It will also be suitable for graduate students specialising in the area of VLSI Digital Signal Processing   **First Principles of Discrete Systems and Digital Signal Processing** Robert D. Strum,Donald E. Kirk,1988 Here is a valuable book for a first undergraduate course in discrete systems and digital signal processing DSP and for in practice engineers seeking a self study text on the subject Readers will find the book easy to read with topics flowing and connecting naturally Fundamentals and first principles central to most DSP applications are presented through carefully developed worked out examples and problems Unlike more theoretically demanding texts this book does not require a prerequisite course in linear systems theory The text focuses on problem solving and developing interrelationships and connections between topics This emphasis is carried out in a number of innovative features including organized procedures for filter design and use of computer based problem solving methods Solutions Manual is available only through your Addison Wesley Sales Specialist   Understanding Digital Signal Processing Richard G. Lyons,2010-11-01 Amazon com s Top Selling DSP Book for Seven Straight Years Now Fully Updated Understanding Digital Signal Processing Third Edition is quite simply the best resource for engineers and other technical

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