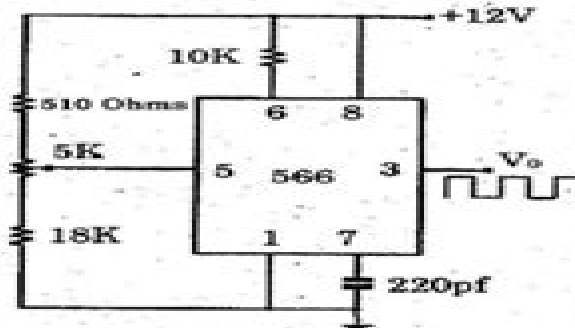


13. (a) Explain the operation of Dual slope ADC with neat illustrations? Also prove that this ADC is free from drifts? (10 + 6)

Or

- (b) With neat schematic representations explain the operation of the following circuits?
- Positive Peak follower.
  - Active positive clamper to clamp the input signal above ground state by 5 V. (8 + 8)
14. (a) Explain with functional block diagram the operation of 566 Voltage Controlled Oscillator. Also determine the maximum and minimum output frequencies in the circuit shown below. (10 + 6)



Or

- (b) Explain the operation of an astable multi-vibrator configured around 555 Timer IC and derive an expression for output frequency with neat illustration.
15. (a) Design a Voltage regulator using IC 723 regulator to satisfy the following specifications
- $V_o = 12 \text{ V}$ .
  - $I_o = 500 \text{ mA}$ .
  - $V_{in} = 18 \pm 20\%$ .
  - $I_{sc} = 600 \text{ mA}$ .
  - $V_{sense} = 0.7 \text{ V}$ . Give the complete schematic diagram. (Assume and Justify if any data required)

Or

- (b) Discuss in detail the operation and applications of the following circuits.
- Isolation amplifier
  - Optocoupler.

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