

## LAB 10

### TTL and CMOS Logic Gates

Reading: Hayes and Horowitz, Class 13 and Lab 13.

Today you will be introduced to the circuits of digital electronics. We will start with some circuits made with discrete electronics to perform logical AND, OR and NOT functions. Next, properties of the most commonly used integrated circuit series, the LS-TTL and HC CMOS, are studied. Finally you will use these basic chips to construct more complex circuits.

#### 1. Mickey Mouse Logic and the Totem Pole Output

These are among the simplest logic devices. They are useful in their own right from time to time. Also, they demonstrate the input circuitry to the most commonly used TTL chips, the low-power Schottky (LS-TTL) family. Here we use standard 1N914 signal diodes instead of the faster Schottky diodes used in LS-TTL gates.

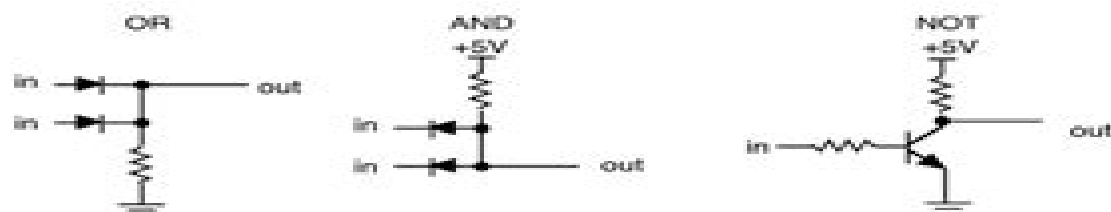


Fig 12.1

The AND circuit illustrated in Fig 12.1 is similar to the input of the 74LS00 NAND gates. Build it and confirm the logic function experimentally and record your results in a truth table. When you test this gate, use 0 V and 5 V for logical false and true.

TTL expects at least 2.0 V input for a high (true) input and guarantees at least 2.4 V for a high output. CMOS requires at least 3.5 V for a high input and delivers at least 4.9 V for high output. In what ways do these circuits disobey these criteria?

# Lab Manual For Logic Gates In Cmos

**Gregory L. Moss**



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### **Table of Contents Lab Manual For Logic Gates In Cmos**

1. Understanding the eBook Lab Manual For Logic Gates In Cmos
  - The Rise of Digital Reading Lab Manual For Logic Gates In Cmos
  - Advantages of eBooks Over Traditional Books
2. Identifying Lab Manual For Logic Gates In Cmos
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Lab Manual For Logic Gates In Cmos
  - User-Friendly Interface
4. Exploring eBook Recommendations from Lab Manual For Logic Gates In Cmos
  - Personalized Recommendations

- Lab Manual For Logic Gates In Cmos User Reviews and Ratings
- Lab Manual For Logic Gates In Cmos and Bestseller Lists
- 5. Accessing Lab Manual For Logic Gates In Cmos Free and Paid eBooks
  - Lab Manual For Logic Gates In Cmos Public Domain eBooks
  - Lab Manual For Logic Gates In Cmos eBook Subscription Services
  - Lab Manual For Logic Gates In Cmos Budget-Friendly Options
- 6. Navigating Lab Manual For Logic Gates In Cmos eBook Formats
  - ePub, PDF, MOBI, and More
  - Lab Manual For Logic Gates In Cmos Compatibility with Devices
  - Lab Manual For Logic Gates In Cmos Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Lab Manual For Logic Gates In Cmos
  - Highlighting and Note-Taking Lab Manual For Logic Gates In Cmos
  - Interactive Elements Lab Manual For Logic Gates In Cmos
- 8. Staying Engaged with Lab Manual For Logic Gates In Cmos
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Lab Manual For Logic Gates In Cmos
- 9. Balancing eBooks and Physical Books Lab Manual For Logic Gates In Cmos
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Lab Manual For Logic Gates In Cmos
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Lab Manual For Logic Gates In Cmos
  - Setting Reading Goals Lab Manual For Logic Gates In Cmos
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Lab Manual For Logic Gates In Cmos
  - Fact-Checking eBook Content of Lab Manual For Logic Gates In Cmos

- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
- 14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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