#### COURSE TITLE: MICROPROCESSOR & MICROCONTROLLERS

Q 1. Consider the least significant 4 digits of your student ID. (For Example, let's say your student id is 1234567, then the 4-least significant digit is 4567). [5 + 2.5 + 2.5 = 10 Marks]

- a. Convert 4-Least Significant decimal digit of your student ID to binary equivalent.
- b. Convert the binary number of above (a) to hexadecimal & octal number format.

Q 2. Differentiate between Microprocessor & Microcontroller with suitable block diagram (Minimum 5 relevant differences).
[5 + 2 = 7 Marks]

Q 3. A long word of 5 bytes of data, 0x12345ABCDE is to be stored in memory location 0x10000A00. Fill the table below following the endianness mentioned. [2.5 + 2.5 = 5 Marks]

Little Endian	
Address	Data
0x10000A00	
0×10000A01	
0x10000A02	
0x10000A03	
0x10000A04	

Big Endian	
Address	Data
0x10000A00	
0x10000A01	
0x10000A02	
0x10000A03	
0x10000A04	

Q 4. Explain the function of the following 8086 microprocessor input/output pins in detail.

[2x 4 = 8 Marks]

a. ALE (Pin 25)

(b) RD' (Pin 32)

(c) NMI (Pin 17)

(d) RESET (Pin 21)

# **Microprocessor And Microcontrollers Solved Papers**

**Michael Brown** 

## **Microprocessor And Microcontrollers Solved Papers:**

Microprocessors & Microcontrollers Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book is written for an undergraduate course on the 8086 microprocessor and 8051 microcontroller It provides comprehensive coverage of the hardware and software aspects of 8086 microprocessor and 8051 microcontroller. The book is divided into three parts The first part focuses on 8086 microprocessor It teaches you the 8086 architecture instruction set Assembly Language Programming ALP interfacing 8086 with support chips memory and peripherals such as 8251 8253 8255 8259 8237 and 8279 It also explains the interfacing of 8086 with data converters ADC and DAC and introduces a traffic light control system The second part focuses on multiprogramming and multiprocessor configurations numeric processor 8087 I O processor 8089 and introduces features of advanced processors such as 80286 80386 80486 and Pentium processors The third part focuses on 8051 microcontroller It teaches you the 8051 architecture instruction set programming 8051 and interfacing 8051 with external memory It explains timers counters serial port interrupts of 8051 and their programming It also describes the interfacing 8051 with data converters ADC and DAC keyboards LCDs LEDs stepper motors and sensors Stage-II Electronics & Allied Engineering Solved Papers YCT Expert Team , 2024 25 RRB JE Stage II Electronics Allied Digest of Technical Papers, 1988 Paper Technology and Industry, 1979 **Engineering Solved Papers** Annual Index/abstracts of SAE Technical Papers ,2007 XIII SBMicro, International Conference on Microelectronics and Packaging, ICMP'98: Technical papers Ivan Jorge Chueiri, Carlos Alberto dos Reis Filho, 1998 **IETE Journal of Research** Second International Conference on Power Electronics and Variable-Speed Drives, 24-26 November .1998 Parallel Problem Solving from Nature--PPSN ... ,1996 **ISIE** ... ,1998 **1986** .1986 CAD/CAM Abstracts ,1989 **Digest of Papers**, 1976 IEEE International Conference on Electronics, Circuits and Systems ,1998 Proceedings of the ... SICE Economic Computation and Economic Cybernetics Studies and Research ,1981 Annual Conference Keisoku Jidō Seigyo Gakkai (Japan). Gakujutsu Kōenkai,1998 Hardware and Software Design Automation Antonio Núñez, Don Fay, 1991 Systems architectures signal processing and systems control design automation parallel processing and software engineering are addressed in a rich variety of high quality papers written by researchers worldwide Presentations and technical discussions of research activity and results obtained in European Community sponsored projects are also included in short notes sections which also include brief up to date reports on recent work in relevant fields and technical reports from industry on new products featuring break throughs or relevant technology This volume should be of special interest to engineers researchers and software developers Programmable Devices and Systems 2001 W. Ciażyński, W. Ciążyński, Edward Hrynkiewicz, P. Kłosowski, 2002 Scientific meetings on programmable devices and systems began in 1995 with the PDS 95 event organised by the Institute of Electronics Silesian University of Technology SUT Many papers on the issues of programmable devices and systems were presented at numerous conferences

and workshops devoted to electronics and circuit theory yet there were no workshops devoted solely to those particular topics Combined with the belief that some specific common problems appeared in the area of PDS justified the decision to organise the PDS meeting The PDS2001 IFAC Workshop organised by the Institute of Electronics SUT Gliwice Poland was the 5th event in the series The aim of the meeting was to define the future trends of this field via the interaction of industry technical research centres and academia representatives This Proceedings volume contains 54 duly presented papers and many of them when compared to the Preprints volume version have been corrected and enriched with the discussion results The papers are grouped according to the Workshop plenary sessions topics as follows bull Communication bull Digital Signal Processing bull Industrial Programmable Logic Controllers bull Field Programmable Logic IECON '98 IEEE Industrial Electronics Society. Conference,1998 Digest of Papers - Compcon ,1976 Indian National Bibliography B. S. Kesavan,2016-12

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