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Multicore Navigator User Guide

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Multicore DSP Naim Dahnoun, 2018-02-12 The only book to offer special coverage of the fundamentals of multicore DSP for implementation on the TMS320C66xx SoC This unique book provides readers with an understanding of the TMS320C66xx SoC as well as its constraints It offers critical analysis of each element which not only broadens their knowledge of the subject but aids them in gaining a better understanding of how these elements work so well together Written by Texas Instruments First DSP Educator Award winner Naim Dahnoun the book teaches readers how to use the development tools take advantage of the maximum performance and functionality of this processor and have an understanding of the rich content which spans from architecture development tools and programming models such as OpenCL and OpenMP to debugging tools It also covers various multicore audio and image applications in detail Additionally this one of a kind book is supplemented with A rich set of tested laboratory exercises and solutions Audio and Image processing applications source code for the Code Composer Studio integrated development environment from Texas Instruments Multiple tables and illustrations With no other book on the market offering any coverage at all on the subject and its rich content with twenty chapters Multicore DSP From Algorithms to Real time Implementation on the TMS320C66x SoC is a rare and much needed source of information for undergraduates and postgraduates in the field that allows them to make real time applications work in a relatively short period of time It is also incredibly beneficial to hardware and software engineers involved in programming real time embedded systems

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